## **CLEANING METHOD OF SEMICONDUCTOR SUBSTRATE**

Patent number:

JP61004232

1986-01-10

**Publication date:** 

TANNO YUKINOBU; TSUJI MIKIO

Inventor:
Applicant:

NIPPON ELECTRIC CO

Classification:

- international:

H01L21/304

- european:

H01L21/306N4

Application number:

JP19840125760 19840619

Priority number(s):

JP19840125760 19840619

Report a data error here

## Abstract of JP61004232

PURPOSE:To safely and readily remove any station on the surface of a silicon wafer, by dipping a semiconductor substrate in a solution of an organic acid being bubbled by ozone or oxygen and thereby treating the substrate. CONSTITUTION:An organic acid (e.g., formic acid or acetic acid) is filled into a cleaning tank and heated (to 100-150 deg.C). A semiconductor substrate is dipped in this liquid, and ozone or oxygen is supplied from the bottom of the tank so as to bubble the liquid, whereby the substrate is cleaned by the bubbles. Any heavy metal on the wafer forms a formate or an acetate, and any organic contaminant is decomposed by ozone, whereby stains on the surface of the substrate can readily be cleaned out.

Data supplied from the esp@cenet database - Worldwide